



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Craig T. Salling et al.

Art Unit: 2815

Serial No.: 10/051,639 ✓

Examiner: Matthew C. Landau

Filed: 01/18/02 ✓

Docket: TI-32536

For: "ESD IMPROVEMENT BY A VERTICAL BIPOLAR TRANSISTOR WITH LOW BREAKDOWN VOLTAGE AND HIGH BETA"

#6/ELECTION  
RECEIVED  
FEB 12 2003  
TECHNOLOGY CENTER 2800  
2/14/03  
P. Hallish

VIA USPS EXPRESS MAIL  
Mailing No. EV 229 128 595 US

Commissioner for Patents  
and Trademarks  
Washington, D.C. 20231

February 10, 2003

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as "Express Mail Post Office to Addressee" under 37 CFR 1.10, on the date shown below and is addressed to: Commissioner for Patents and Trademarks, Washington, D.C. 20231.

*Ginger Cox*  
Ginger Cox

*02/10/2003*  
Date of Deposit

ELECTION

Sir:

Responsive to the Office Action dated January 16, 2003, please enter the following:

For prosecution, Applicants elect Group I, Claims 1-8 and 19, drawn to a semiconductor device, classified in class 257, subclass 565.

Applicants acknowledge that Group II, Claims 9-18, will be withdrawn from further examination.

A prompt action on the merits is respectfully requested.

Respectfully submitted,

*Gary C. Honeycutt*  
\_\_\_\_\_  
Gary C. Honeycutt  
Reg. No. 20,250

Godwin Gruber, LLP  
Renaissance Tower  
1201 Elm Street, Suite 1700  
Dallas, Texas 75270-2084  
Phone (214) 939-8651  
Fax (214) 760-7332